

Appl. No. 10/725,050
Examiner: FENTY, JESSE A, Art Unit 2815
Supplemental amendment in response to the Office Action
dated March 22, 2005

Date: July 7, 2005
Attorney Docket No. 10111682

REMARKS

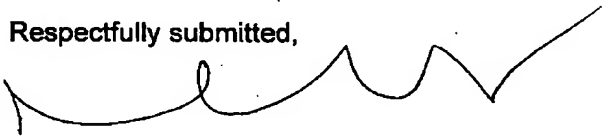
Pursuant to 37 C.F.R. 1.111(a)(2)(E), Applicant respectfully requests entry of this supplement amendment to correct informalities in the specification.

This amendment corrects an informality in the original specification. Namely, this amendment cancels a typographical error on page 7, lines 19-20 indicating that the first bottom portion contacts the semiconductor substrate 201, when, as shown in Fig. 2G and described in the following sentence, a gate dielectric layer 202 is formed between conducting layer 203 and the semiconductor substrate 201. This brings specification into correspondence with the amendment to claim 1 presented in the response filed on June 22, 2005.

Conclusion

The Applicant believes that the application is now in condition for allowance and respectfully requests so.

Respectfully submitted,



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